



## Product/Process Change Notice - PCN 20\_0295 Rev. -

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This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

**PCN Title:** ADR504x Family Data Sheet Change

**Publication Date:** 17-Nov-2020

**Effectivity Date:** 17-Nov-2020 *(the earliest date that a customer could expect to receive changed material)*

**Revision Description:**

Initial Release.

**Description Of Change:**

Page 1: Change to note in Pin Configuration section from "IT IS RECOMMENDED THAT PIN 3 BE CONNECTED TO V-" to "IT IS RECOMMENDED THAT PIN 3 BE CONNECTED TO V+".

**Reason For Change:**

The data sheet is being updated to accurately reflect device capabilities.

**Impact of the change (positive or negative) on fit, form, function & reliability:**

No change to fit or form. Improved reliability under certain brown out conditions.

**Summary of Supporting Information:**

Changes are reflected on the new Product Data Sheet rev. E on page# 1.

**Comments**

The power-on reset (POR) circuit may not activate correctly during very short duration power supply brown-out (<100ns). If the brown out reaches a low enough voltage level the trim cell latches may lose power and not be reset to their correct values since the POR circuit did not trigger. In this case the output can be programmed (trimmed) to a different voltage.

Both conditions to cause this situation are only possible in a narrow range of brown-out duration and voltage level dip. The change in output voltage is temporary until the part is reset by a power cycle.

Connecting pin 3 to V+ instead of V- will eliminate the possibility of this issue occurring.

**Supporting Documents**

**Attachment 1: Type:** Delta Qualification Matrix

ADI\_PCN\_20\_0295\_Rev\_-\_ADR504x\_DELTA QUALIFICATION MATRIX-ZVEI-4\_1.xlsm

**For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.**

**Americas:**  
PCN\_Americas@analog.com

**Europe:**  
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PCN\_Japan@analog.com

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**Appendix A - Affected ADI Models****Added Parts On This Revision - Product Family / Model Number (27)**

ADR5040 / ADR5040AKSZ-REEL7	ADR5040 / ADR5040ARTZ-REEL7	ADR5040 / ADR5040BKSZ-REEL7	ADR5040 / ADR5040BRTZ-REEL7	ADR5040 / ADR5040WARTZ-R7
ADR5041 / ADR5041AKSZ-REEL7	ADR5041 / ADR5041ARTZ-REEL7	ADR5041 / ADR5041BKSZ-REEL7	ADR5041 / ADR5041BRTZ-REEL7	ADR5041 / ADR5041WARTZ-R7
ADR5041 / ADR5041WBRTZ-R7	ADR5043 / ADR5043AKSZ-REEL7	ADR5043 / ADR5043ARTZ-REEL7	ADR5043 / ADR5043BKSZ-REEL7	ADR5043 / ADR5043BRTZ-REEL7
ADR5044 / ADR5044AKSZ-REEL7	ADR5044 / ADR5044ARTZ-REEL7	ADR5044 / ADR5044BKSZ-REEL7	ADR5044 / ADR5044BRTZ-REEL7	ADR5044 / ADR5044WARTZ-R7
ADR5044 / ADR5044WBRTZ-R7	ADR5045 / ADR5045AKSZ-REEL7	ADR5045 / ADR5045ARTZ-REEL7	ADR5045 / ADR5045BKSZ-REEL7	ADR5045 / ADR5045BRTZ-REEL7
ADR5045 / ADR5045WARTZ-REEL7	ADR5045 / ADR5045WBRTZ-REEL7			

**Appendix B - Revision History**

<b>Rev</b>	<b>Publish Date</b>	<b>Effectivity Date</b>	<b>Rev Description</b>
Rev. -	17-Nov-2020	17-Nov-2020	Initial Release.

Analog Devices, Inc.

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